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Applicants: Eric Lee, Dave Cobert and Wanqing Cao  
Assignee: Integrated Device Technology, Inc.  
Title: CONFORMAL SURFACE SILICIDE STRAP ON SPACER  
(As amended)

Serial No.: 09/772,115  
Filed: 01/26/01

Attorney Docket No.: IDT-1641

Group No.: 2811

Examiner: D. Owens

TECHNOLOGY CENTER 2800

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ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

Date: January 25, 2002

Sir:

RESPONSE TO FIRST OFFICE ACTION

Applicants submit the following remarks and amendments  
in response to the Office Action dated October 25, 2001.

~~Change~~ the title to "Conformal Surface Silicide Strap  
On Spacer".

IN THE TITLE

IN THE CLAIMS

Replace Claim 8 with the following new Claim 8:

8. (Amended) A semiconductor structure comprising:  
a semiconductor substrate;  
a conductive element located over the  
semiconductor substrate;  
a dielectric spacer located adjacent to a  
sidewall of the conductive element; and  
a continuous silicide strap located over the  
conductive element, the dielectric spacer and the